



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Ference et al.

Confirmation No.: 9501

Serial No.: 09/732,240

Group Art Unit: 2815

Filed: 12/07/00

Examiner: Chris C. Chu

Title:

MULTI-CHIP STACK AND METHOD OF FABRICATION UTILIZING
SELF-ALIGNING ELECTRICAL CONTACT ARRAY

#13/AndH0
RT
7/12/03

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited
with the U.S. Postal Service as first class mail in an envelope
addressed to: Mail Stop Non-Fee Amendment, Commissioner
for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on
July 08, 2003.

Kevin P. Radigan
Kevin P. Radigan
Attorney for Applicants
Registration No.: 31,789

Date of Signature: July 08, 2003.

To: Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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TECHNOLOGY CENTER 2800

Response To Office Action

Dear Sir:

This paper is submitted in reply to the Office Action mailed April 8, 2003, in
connection with the above-designated application. Reconsideration and allowance of the
application are respectfully requested in view of the amendments and remarks below.